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**DATE:** Wednesday, October 06, 2004    [Printable Copy](#)    [Create Case](#)

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DB=USPT; PLUR=YES; OP=ADJ			
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<u>L4</u>	l2 and (computer\$ or cpu or microprocess\$6 or microcomput\$3) same clock\$3 same tim\$3	1	<u>L4</u>
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<u>L2</u>	L1 and (access\$6 or generat\$6) same (stor\$3 or repositor\$3) same (data or information)	2	<u>L2</u>
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L4: Entry 1 of 1

File: USPT

Feb 16, 1999

DOCUMENT-IDENTIFIER: US 5873108 A

TITLE: Personal information manager information entry allowing for intermingling of items belonging to different categories within a single unified view

Brief Summary Text (7):

One example of such a personal information manager is found in U.S. Pat. No. 5,237,651, entitled ELECTRONIC PERSONAL ORGANIZER, incorporated herein by reference. The electronic personal organizer described therein electronically displays graphics representative of pages of a loose-leaf booklet or binder arranged into various sections on a display screen. User input to the organizer is accomplished through a stylus and an input tablet or transducer pad sensitive to pressure or position of the stylus. Input and storage of user-generated graphics, notes, addresses, messages, etc., is provided for through various user-selectable operational modes. For example, to look up an address, an "address operational mode" is entered by positioning the stylus within the displayed PHONE tab. The organizer then operates as an address book by displaying representational graphics depicting a conventional organizer open at its address section. The edges of a sheath of pages are displayed beneath a front page having a tab indexed by the letter "A". Further tabs are shown defining sections of the sheath and each marked by a letter of the alphabet. The address book is opened to the appropriate section by positioning the stylus within the area of the desired tab. Pages are then "leafed through" using page forward and page back commands until the desired address is located.

Detailed Description Text (3):

Referring to FIG. 2, in one exemplary embodiment, the computing device 100 of FIG. 1 includes a microprocessor 201 and, connected to the microprocessor 201, control logic 203. Together, the microprocessor 201 and the control logic 203 make up the device "core." The microprocessor 201 and the control logic 203 are connected to a central bus 205 including address, data and control lines. Also connected to the bus 205 are memory devices, including a static RAM 207 and an EPROM 209, a PCMCIA connector 211, a real-time clock 213, an LCD controller 215 and a touch-screen interface 217. The LCD controller 215 is connected in turn to video RAM 219 and the LCD display 220. The touch-screen interface 217 is connected to a touch-screen 221. Together, the LCD display 220 and the touch-screen 221 form the LDC touch-screen display 101 of FIG. 1.

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